

Certificate of compliance

Applicant: NingBo Deye Inverter Technology Co., Ltd.
No. 26 South YongJiang Road, Daqi, Beilun, NingBo
China

Product: Photovoltaic and battery inverter

Model: SUN-5K-SG04LP1-EU
SUN-5K-SG04LP1-EU-SM2
SUN-6K-SG04LP1-EU
SUN-6K-SG04LP1-EU-SM2

Use in accordance with regulations:

Automatic disconnection device with single-phase mains surveillance in accordance with Engineering Recommendation G99/1 for photovoltaic systems with a single-phase parallel coupling via an inverter in the public mains supply. The automatic disconnection device is an integral part of the aforementioned inverter. This serves as a replacement for the disconnection device with isolating function, which can be accessed the distribution network provider at any time.

Applied rules and standards:

Engineering Recommendation G99/1-10:2024

Requirements for the connection of generation equipment in parallel with public distribution networks

DIN V VDE V 0126-1-1:2006 (4.1 Functional safety)

Automatic disconnection device between a generator and the public low-voltage grid

At the time of issue of this certificate, the safety concept of an aforementioned representative product corresponds to the valid safety specifications for the specified use in accordance with regulations.

Report number: ASUE-ESH-P25050530

Certificate number: U25-0540

Certification Program: NSOP-0032-DEU-ZE-V10

Date of issue: 2025-06-06

Certification body

Accreditation



Domenik Koll
Head of Energy Systems Germany



Accredited certification body by Deutsche Akkreditierungsstelle GmbH (DAkkS) according to ISO/IEC 17065. The accreditation is valid only for the scope listed in the annex of the accreditation certificate D-ZE-12024-01-00. The Deutsche Akkreditierungsstelle GmbH (DAkkS) is signatory of the multilateral arrangements of EA, ILAC and IAF for mutual recognition.

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Appendix A2-3 Compliance Verification Report for Inverter Connected Power Generating Modules

Extract from test report according to the Engineering Recommendation G99/1

Type Approval and declaration of compliance with the requirements of Engineering Recommendation G99/1.

PGM Technology	Photovoltaic and battery inverter		
Manufacturer	NingBo Deye Inverter Technology Co., Ltd.		
Address	No. 26 South YongJiang Road, Daqi, Beilun, NingBo, China		
Tel	--	Fax	--
Email	--	Website	--

PGM Model	SUN-5K-SG04LP1-EU	SUN-5K-SG04LP1-EU-SM2	SUN-6K-SG04LP1-EU	SUN-6K-SG04LP1-EU-SM2
Input (DC photovoltaic)				
MPP voltage range [V]	150-425	150-425	150-425	150-425
Max. input voltage [V]	500	500	500	500
Max. input current per MPPT [A]	18+18	18+18	18+18	18+18
Output (AC)				
Rated AC voltage [V]	230, L/N/PE, 50Hz	230, L/N/PE, 50Hz	230, L/N/PE, 50Hz	230, L/N/PE, 50Hz
Rated output current [A]	21,7	21,8	26,1	26,1
Max. output current [A]	23,9	24,0	28,7	28,7
Nom. converter output (P_{NINV}) [kW]	5,00	5,00	6,00	6,00
Rated apparent power [kVA]	5,50	5,50	6,60	6,60

Interface protection system and interface switch (Network and system protection "NS-protection")

Type of protection	integrated NS-protection
Assigned to generation unit type	SUN-5K-SG04LP1-EU SUN-5K-SG04LP1-EU-SM2 SUN-6K-SG04LP1-EU SUN-6K-SG04LP1-EU-SM2
Integrated interface switch	Type of switching equipment 1: Relay (Model AZSR143) Type of switching equipment 2: Relay (Model AZSR143) Note: The output is switched off by the inverter bridge and two relay in series in each line and neutral.
Firmware version	4384

Differences between Generating Units

Reference Position	SUN-5K-SG04LP1-EU	SUN-6K-SG04LP1-EU	Comments
BOOST1, BOOST2, INV1, INV2	(BOOST1+BOOST2+INV1+INV2)	(BOOST1+BOOST2+INV1+INV2)	Inverter Choke
C1, C2, C3, C10	315V/1000uF	315V/1000uF	Inverter Bus Capacitor
Q30, Q31, Q34, Q35	CRSQ027N10N	CRSQ027N10N	DCDC front stage NMOSFET
PV input connector in parallel	1+1	1+1	Number of input connectors
Fan	NC	1	External fan

Reference Position	SUN-5K-SG04LP1-EU-SM2	SUN-6K-SG04LP1-EU-SM2	Comments
BOOST1, BOOST2, INV1, INV2	(BOOST1+BOOST2+INV1+INV2)	(BOOST1+BOOST2+INV1+INV2)	Inverter Choke
C1, C2, C3, C10	315V/1000uF	315V/1000uF	Inverter Bus Capacitor
Q30, Q31, Q34, Q35	CRSQ027N10N	CRSQ027N10N	DCDC front stage NMOSFET
PV input connector in parallel	1+1	1+1	Number of input connectors
W*H*D(mm)	327*363*88	327*363*88	Heat sink

The above stated Generating Units are tested according the requirements in the Engineering Recommendation G99/1. Any modification that affects the stated tests must be named by the manufacturer/supplier of the product to ensure that the product meets all requirements of the Engineering Recommendation G99/1.

Operating Range.	
Test 1	Voltage = 85% of nominal (195,5V) Frequency = 47Hz Power Factor = 1 Period of test 20 s
Connection	Always connected
Limit	No disconnection allowed
Test 2	Voltage = 85% of nominal (195,5V) Frequency = 47,5Hz Power Factor = 1 Period of test 90 minutes
Connection	Always connected
Limit	No disconnection allowed
Test 3	Voltage = 110% of nominal (253V) Frequency = 51,5Hz Power Factor = 1 Period of test 90 minutes
Connection	Always connected
Limit	No disconnection allowed
Test 4	Voltage = 110% of nominal (253V) Frequency = 52,0Hz Power Factor = 1 Period of test 15 minutes
Connection	Always connected
Limit	No disconnection allowed
Test 5	Voltage = 100% of nominal (230 V) Frequency = 50,0 Hz Power Factor = 1 Period of test 90 minutes
Connection	Always connected
Limit	No disconnection allowed
Test 6	Confirm that the Power Generating Module is capable of staying connected to the Distribution Network and operate at rates of change of frequency up to 1 Hzs-1 as measured over a period of 500ms. Note that this is not expected to be demonstrated on site.
Connection	Always connected
Limit	No disconnection allowed

Protection. Voltage tests.

Phase 1

Function	Setting		Trip test		No trip test	
	Voltage [V]	Time delay [s]	Voltage [V]	Time delay [s]	Voltage / time	Confirm no trip
U/V	184	2,5	184,0	2,511	188V / 5,0s	No trip
					180V / 2,45s	No trip
O/V stage 1	262,2	1,0	262,2	1,006	258,2V 5,0s	No trip
O/V stage 2	273,7	0,5	273,7	0,508	269,7V 0,95s	No trip
					277,7V 0,45s	No trip

Note. For Voltage tests the Voltage required to trip is the setting $\pm 3,45V$. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting $\pm 4V$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Frequency tests.

Function	Setting		Trip test		No trip test	
	Frequency [Hz]	Time delay [s]	Frequency [Hz]	Time delay [s]	Frequency / time	Confirm no trip
U/F stage 1	47,5	20	47,49	20,021	47,7Hz / 30s	No trip
U/F stage 2	47	0,5	46,99	0,506	47,2Hz / 19,5s	No trip
					46,8Hz / 0,45s	No trip
O/F stage 2	52	0,5	52,00	0,506	51,8Hz / 120s	No trip
					52,2Hz / 0,45s	No trip

Note. For Frequency Trip tests the Frequency required to trip is the setting $\pm 0,1Hz$. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No-trip tests" need to be carried out at the setting $\pm 0,2Hz$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Loss of Mains.

Inverters tested according to BS EN 62116.

Balancing load on islanded network	33% of -5% Q Test 22	66% of -5% Q Test 12	100% of -5% P Test 5	33% of +5% Q Test 31	66% of +5% Q Test 21	100% of +5% P Test 10
Trip time. Ph1 fuse removed [ms]	170,9	187,4	395,2	182,1	196,2	366,9

Note. Trip time limit is 0,5s.

Protection. Re-connection timer.

Test should prove that the reconnection sequence starts in no less than 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 10.1.

Over Voltage (258,2 V)				
Time delay setting [s]		Measured delay [s]		
20		74,3		
Under Voltage (188,0 V)				
Time delay setting [s]		Measured delay [s]		
20		73,6		
Over Frequency (51,9 Hz)				
Time delay setting [s]		Measured delay [s]		
20		73,8		
Under Frequency (47,6 Hz)				
Time delay setting [s]		Measured delay [s]		
20		74,5		
	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
	At 266,2V	At 180,0V	At 47,4Hz	At 52,1Hz
Confirmation that the Generating Unit does not re-connect.	No reconnection	No reconnection	No reconnection	No reconnection

Protection. Frequency change, Stability test.

	Start Frequency [Hz]	Change	Test Duration	Confirm no trip
Positive Vector Shift	49,5	+50 degrees		No trip
Negative Vector Shift	50,5	-50 degrees		No trip
Positive Frequency drift	49,0 to 51,0	+0,95Hz/sec	2,1s	No trip
Negative Frequency drift	51,0 to 49,0	-0,95Hz/sec	2,1s	No trip

Limited Frequency Sensitive Mode – Over Frequency							
1-min mean value [Hz]:	a) 50,00	b) 50,45	c) 50,70	d) 51,15	e) 50,70	f) 50,45	g) 50,00
1. Measurement a) to g): Active power output > 80% P _n							
Frequency [Hz]	50,00	50,45	50,70	51,15	50,70	50,45	50,00
P _{expected} [kW]	6,00	5,94	5,64	5,10	5,64	5,94	6,00
P _{measured} [kW]	6,00	5,93	5,64	5,08	5,64	5,93	6,00
2. Measurement a) to g): Active power output 40% and 60% P _n							
Frequency [Hz]	50,00	50,45	50,70	51,15	50,70	50,45	50,00
P _{expected} [kW]	3,00	2,94	2,64	2,10	2,64	2,94	6,00
P _{measured} [kW]	3,01	2,95	2,65	2,11	2,65	2,95	6,00

Output Power with falling Frequency						
Frequency setpoint [Hz]	50,00	49,50	49,00	48,00	47,60	47,10
Frequency [Hz]	50,00	49,50	49,00	48,00	47,60	47,10
Active power [kW]	6,00	6,00	5,99	5,99	5,99	5,99
ΔP/P _{max} [%]		0,00	-0,17	-0,17	-0,17	-0,17
Note. No Power reduction take place for electronic inverter						

Power Quality. Harmonics.						
Generating Unit rating per phase (rpp)			SUN-6K-SG04LP1-EU			
	At 45-55% of rated output 3,01 kW		100% of rated output 5,99 kW			
Harmonic	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Limit in BS EN61000-3-12 in %	
					1 phase	3 phase
2nd	0,022	0,085	0,028	0,107	8%	8%
3rd	0,007	0,028	0,012	0,044	21,6%	N/A
4th	0,018	0,067	0,031	0,119	4%	4%
5th	0,048	0,185	0,088	0,339	10,7%	10,7%
6th	0,004	0,017	0,006	0,022	2,67%	2,67%
7th	0,020	0,075	0,048	0,183	7,2%	7,2%
8th	0,010	0,040	0,010	0,037	2%	2%
9th	0,002	0,008	0,004	0,016	3,8%	N/A
10th	0,007	0,029	0,009	0,036	1,6%	1,6%
11th	0,078	0,301	0,167	0,641	3,1%	3,1%
12th	0,006	0,022	0,006	0,023	1,33%	1,33%
13th	0,071	0,274	0,112	0,429	2%	2%
14th	0,006	0,023	0,007	0,026	N/A	N/A
15th	0,005	0,019	0,007	0,025	N/A	N/A
16th	0,004	0,015	0,004	0,014	N/A	N/A
17th	0,043	0,166	0,061	0,232	N/A	N/A
18th	0,006	0,025	0,005	0,020	N/A	N/A
19th	0,045	0,171	0,062	0,239	N/A	N/A
20th	0,003	0,010	0,003	0,011	N/A	N/A
21th	0,005	0,020	0,005	0,020	N/A	N/A
22th	0,005	0,020	0,005	0,018	N/A	N/A
23th	0,024	0,091	0,044	0,169	N/A	N/A
24th	0,004	0,014	0,003	0,012	N/A	N/A
25th	0,032	0,123	0,036	0,137	N/A	N/A
26th	0,004	0,015	0,003	0,012	N/A	N/A
27th	0,004	0,016	0,005	0,019	N/A	N/A
28th	0,003	0,013	0,004	0,015	N/A	N/A
29th	0,019	0,073	0,028	0,106	N/A	N/A
30th	0,004	0,014	0,003	0,013	N/A	N/A
31th	0,021	0,080	0,028	0,107	N/A	N/A
32th	0,005	0,019	0,004	0,015	N/A	N/A
33th	0,006	0,022	0,005	0,021	N/A	N/A
34th	0,004	0,014	0,004	0,017	N/A	N/A
35th	0,018	0,070	0,029	0,110	N/A	N/A
36th	0,004	0,016	0,003	0,013	N/A	N/A
37th	0,015	0,057	0,013	0,048	N/A	N/A
38th	0,005	0,019	0,004	0,014	N/A	N/A
39th	0,004	0,016	0,002	0,009	N/A	N/A
40th	0,005	0,019	0,004	0,016	N/A	N/A
THD ₄₀ [%]	--	1,414	--	0,987	23%	13%
PWHD [%]	--	0,550	--	0,617	23%	22%

Power Quality. Harmonics.

Generating Unit rating per phase (rpp)			SUN-5K-SG04LP1-EU			
	At 45-55% of rated output 2,49 kW		100% of rated output 5,00 kW			
Harmonic	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Limit in BS EN61000-3-12 in %	
					1 phase	3 phase
2nd	0,035	0,163	0,084	0,388	8%	8%
3rd	0,009	0,041	0,026	0,119	21,6%	N/A
4th	0,031	0,145	0,047	0,215	4%	4%
5th	0,042	0,193	0,069	0,317	10,7%	10,7%
6th	0,005	0,023	0,009	0,040	2,67%	2,67%
7th	0,022	0,099	0,033	0,153	7,2%	7,2%
8th	0,006	0,027	0,006	0,027	2%	2%
9th	0,003	0,014	0,011	0,048	3,8%	N/A
10th	0,005	0,023	0,006	0,028	1,6%	1,6%
11th	0,067	0,309	0,145	0,669	3,1%	3,1%
12th	0,003	0,013	0,004	0,017	1,33%	1,33%
13th	0,055	0,251	0,092	0,423	2%	2%
14th	0,004	0,021	0,008	0,039	N/A	N/A
15th	0,003	0,014	0,008	0,035	N/A	N/A
16th	0,003	0,013	0,003	0,016	N/A	N/A
17th	0,041	0,187	0,059	0,272	N/A	N/A
18th	0,003	0,014	0,005	0,021	N/A	N/A
19th	0,031	0,143	0,049	0,227	N/A	N/A
20th	0,005	0,025	0,007	0,030	N/A	N/A
21th	0,005	0,025	0,008	0,039	N/A	N/A
22th	0,004	0,018	0,006	0,027	N/A	N/A
23th	0,029	0,132	0,044	0,205	N/A	N/A
24th	0,003	0,016	0,005	0,021	N/A	N/A
25th	0,021	0,096	0,028	0,130	N/A	N/A
26th	0,005	0,024	0,005	0,024	N/A	N/A
27th	0,004	0,016	0,006	0,029	N/A	N/A
28th	0,005	0,022	0,008	0,038	N/A	N/A
29th	0,019	0,088	0,025	0,117	N/A	N/A
30th	0,004	0,019	0,005	0,023	N/A	N/A
31th	0,018	0,085	0,028	0,130	N/A	N/A
32th	0,003	0,014	0,003	0,016	N/A	N/A
33th	0,003	0,014	0,004	0,017	N/A	N/A
34th	0,005	0,023	0,006	0,028	N/A	N/A
35th	0,017	0,079	0,024	0,112	N/A	N/A
36th	0,003	0,015	0,003	0,015	N/A	N/A
37th	0,010	0,044	0,019	0,086	N/A	N/A
38th	0,003	0,014	0,003	0,013	N/A	N/A
39th	0,005	0,023	0,003	0,014	N/A	N/A
40th	0,004	0,019	0,006	0,025	N/A	N/A
THD ₄₀ [%]	--	1,216	--	1,102	23%	13%
PWHD [%]	--	0,510	--	2,344	23%	22%

Power Quality. Power factor.

Output power	216,2V	230V	253V	Measured at three voltage levels and at full output. Voltage to be maintained within $\pm 1,5\%$ of the stated level during the test.
20%	0,9984	0,9981	0,9977	
50%	0,9997	0,9996	0,9996	
75%	0,9998	0,9998	0,9998	
100%	0,9999	0,9998	0,9999	
Limit	>0,95	>0,95	>0,95	

Power Quality. Voltage fluctuation and Flicker.

	Starting			Stopping			Running	
	dmax	dc	d(t)	dmax	dc	d(t)	Pst	Plt 2 hours
Measured values at test impedance	0,426	0,166	0,00	0,487	0,187	0,00	0,120	0,122
Measured values at standard impedance	0,426	0,166	0,00	0,487	0,187	0,00	0,120	0,122
Values for maximum impedance	0,426	0,166	0,00	0,487	0,187	0,00	0,120	0,122
Limits set under BS EN 61000-3-11	4%	3,3%	3,3% 500ms	4%	3,3%	3,3% 500ms	1,0	0,65
Test impedance	R	0,24	Ω	XI	0,15	Ω		
	Z	0,28	Ω					
Standard impedance	R	0,24	Ω	XI	0,15	Ω		
	Z	0,28	Ω					
Maximum impedance	R	0,24	Ω	XI	0,15	Ω		
	Zmax	0,28	Ω					

Power Quality. DC injection.			
SUN-6K-SG04LP1-EU			
Phase 1			
Test level power [%]	10	55	100
Recorded value [mA]	44,8	41,9	45,3
Recorded value [%]	0,17	0,16	0,17
Limit [%]	0,25	0,25	0,25
Phase 2			
Test level power [%]	10	55	100
Recorded value [mA]	-	-	-
Recorded value [%]	-	-	-
Limit [%]	-	-	-
Phase 3			
Test level power [%]	10	55	100
Recorded value [mA]	-	-	-
Recorded value [%]	-	-	-
Limit [%]	-	-	-
Note. Informative measurement of DC-injection of each phase of the inverter and a limit of 0,25% per phase of the rated current per phase as pass criteria.			
Sum of all Phases Tests are carried out at three defined power levels $\pm 5\%$. At 230 V a 6,0 kW three phase Inverter has a current output of 26,1 A so DC limit is 65,2 mA. These tests is undertaken in accordance with Annex A.7.1.4.4. The % DC injection (“as % of rated AC current” below) is calculated as follows: $\% \text{ DC injection} = \text{Recorded DC value in Amps} / \text{Base current where the base current is the Registered Capacity (W) / V phase.}$ The % DC injection should not be greater than 0,25%.			
Sum of all Phases			
Test level power [%]	10	55	100
Recorded value [mA]	44,8	41,9	45,3
Recorded value [%]	0,17	0,16	0,17
Limit [%]	0,25	0,25	0,25

Fault level Contribution.					
For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after fault	Volts [V]	Amps [A]
Peak Short Circuit current	I_p	N/A	20ms	13,07	0,04
Initial Value of aperiodic current	A	N/A	100ms	10,80	0,10
Initial symmetrical short-circuit current*	I_k	N/A	250ms	16,78	0,00
Decaying (aperiodic) component of short circuit current*	i_{DC}	N/A	500ms	11,26	0,00
Reactance/Resistance Ratio of source*	X/R	N/A	Time to Trip [s]	0,002	
For rotating machines and linear piston machines the test should produce a 0s – 2s plot of the short circuit current as seen at the Generating Unit terminals.					
* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot.					

Self Monitoring – Solid state switching.	N/A
It has been verified that in the event of the solid state switching device failing to disconnect the Power Park Module, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0,5 seconds.	N/A
Note. Unit do not provide solid state switching relays. In case the semiconductor bridge is switched off, then the voltage on the output drops to 0. In this case the relays on the output will also open (Functional safety of the internal automatic disconnection device according to VDE 0126-1-1 / VDE 0124-100).	

Cyber security	P
Confirm that the Manufacturer or Installer of the Micro-generator has provided a statement describing how the Micro-generator has been designed to comply with cyber security requirements, as detailed in 9.7.	Yes
Note. Different levels of access, all are password protected, only certain parameters can be changed on maintenance level.	

NINGBO DEYE INVERTER TECHNOLOGY CO.,LTD
ADDRESS:NO.26 SOUTHERN YONGJIANG ROAD,BEILUN,NINGBO CHINA
Daiyd@deye.com.cn
TEL:0086-574-86228957
FAX:0086-574-86228957



Manufacturer's declaration

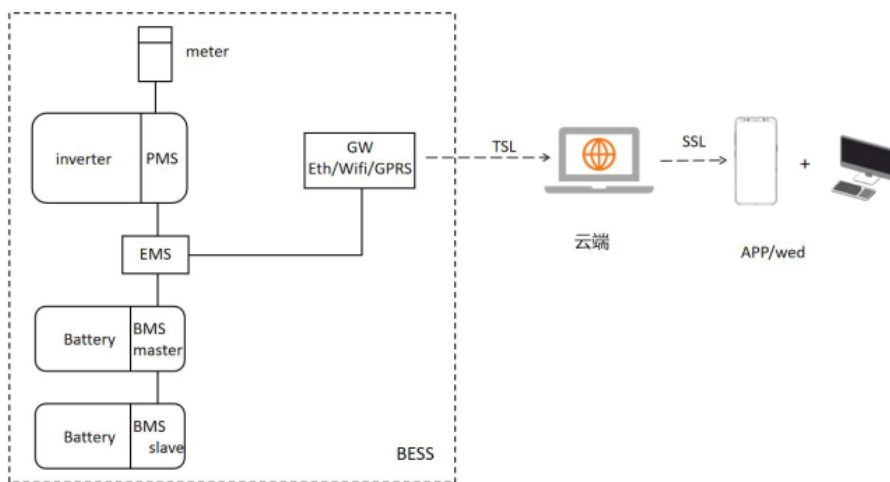
NingBo Deye Inverter Technology Co.,Ltd.

No.26 South YongJiang Road, Daqi, Beilun, NingBo, China.

The undersigned Bard Dai as Certification Manager of the Company NingBo Deye Inverter Technology Co.,Ltd.On behalf of the same Company declares the following:

The inverter in accordance with the requirements of G98-Amd. 6 (2021-09) standard Sec.s 9.7.1, 9.7.2, and G99-Amd. 8 (2021-09) standard Sec.s 9.1.7, 9.1.8 regarding "Cyber Security"

The NingBo Deye Inverter Technology Co.,Ltd. Battery Energy Storage Systems (BESS) include a system of internal and external logic communications as summarized in the following scheme:



where the main components involved and their main functions are explained in the following table:

acronym/ name	meaning	function	location
PMS	Power Management System	monitoring and management of power fluxes through the inverter, execution of EMS's commands or local logic functions depending on grid parameters values <i>Note: The PMS performs operational safety functions aimed at prevent physical damage/harm, typically by interrupting currents and/or opening contacts on some inverter ports when voltage, current or temperature limits are violated; no safety operation performed by PMS can be compromised/skipped by commands/signals originating outside the inverter.</i>	inverter
BMS	Battery Management System	monitoring of cells's status, execution of PMS's commands within safety conditions <i>Note: The BMS performs operational safety functions aimed at prevent physical damage/harm, typically by interrupting currents and/or opening contacts on some battery or BMS ports when voltage, current or temperature limits are violated; no safety operation performed by BMS can be compromised/skipped by commands/signals originating outside the BMS and batteries.</i>	battery
EMS	Energy Management System	monitoring of all field's measures, calculus of power and currents for every component of the system, reception of external commands, transmission of commands to PMS. <i>Note: No operational safety function aimed at preventing physical damage/harm is performed by the EMS; no operation performed by EMS can force the operational safety functions performed by BMS, PMS and electrical protections.</i>	Monitor Board
GW	Gate-Way	transmission of data to cloud server, reception of commands/settings from external stakeholder	Stick Logger
Meter	External Power Meter	meter at AC port of third party generator/inverter, for power measures	Meter

- 1) All communications between internal components of the BESS, and between EMS and supplied External Power Meter(s), take place via appropriate serial lines (RS485, CanBus) and are not directly connected to any device or system outside the BESS.
- 2) The only communication port between the BESS and the outside is constituted by the Gate-Way layer (GW) on the machine; the communication between BESS and the outside world can take place via an Ethernet line, WiFi or GPRS router according to the customer's request.
The BESS is a not-constrained customer IoT device according to the definitions in ETSI EN 303 645 sec. 3.1.
- 3) The direct recipients/senders of communications with the BESS is the in-cloud server –the communication is made secure by the use of TSL (Transport Layer Security) technology on GW, and by the use of SSL (Secure Sockets Layer) technology on Final User's device side and web-tools side;
- 4) The cyber-security assessment of the BESS was performed according to the ETSI EN 303 645 standard, and it is reported according to the Table B.1 form of the same standard:

EN 303 645 v2.1.1 (2020-06) Table B.1: Implementation of provisions for consumer IoT security			
Clause number and title			
Reference	Status	Support	Detail
5.1 No universal default passwords			
Provision 5.1-1	M C (1)	N/A	Device don not permit final user's login
Provision 5.1-2	M C (2)	N/A	
Provision 5.1-3	M	N/A	
Provision 5.1-4	M C (8)	N/A	
Provision 5.1-5	M C (5)	N/A	
5.2 Implement a means to manage reports of vulnerabilities			
Provision 5.2-1	M	Y	
Provision 5.2-2	R	Y	
Provision 5.2-3	R	Y	
5.3 Keep software updated			
Provision 5.3-1	R	Y	
Provision 5.3-2	M C (5)	Y	
Provision 5.3-3	M C (12)	Y	
Provision 5.3-4	R C (12)	Y	The manufacturer manages the updates of the systems by means of remote automatisms, selectively by type of machine or by activating special functions at the request of the user
Provision 5.3-5	R C (12)	N	see note at 5.3-4
Provision 5.3-6	R C (9, 12)	N	see note at 5.3-4

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EN 303 645 v2.1.1 (2020-06) Table B.1: Implementation of provisions for consumer IoT security			
Clause number and title			
Reference	Status	Support	Detail
Provision 5.3-7	M C (12)	Y	
Provision 5.3-8	M C (12)	N	see note at 5.3-4
Provision 5.3-9	R C (12)	N	
Provision 5.3-10	M (11, 12)	Y	
Provision 5.3-11	R C (12)	N	
Provision 5.3-12	R C (12)	N	
Provision 5.3-13	M	Y	
Provision 5.3-14	R C (3, 4)	N/A	not constrained device
Provision 5.3-15	R C (3, 4)	N/A	not constrained device
Provision 5.3-16	M	Y	
5.4 Securely store sensitive security parameters			
Provision 5.4-1	M	Y	
Provision 5.4-2	M C (10)	Y	
Provision 5.4-3	M	N/A	hard-coded identity not used in source code
Provision 5.4-4	M	Y	
5.5 Communicate securely			
Provision 5.5-1	M	Y	
Provision 5.5-2	R	Y	
Provision 5.5-3	R	Y	
Provision 5.5-4	R	N	
Provision 5.5-5	M	Y	
Provision 5.5-6	R	Y	
Provision 5.5-7	M	Y	
Provision 5.5-8	M	Y	
5.6 Minimize exposed attack surfaces			
Provision 5.6-1	M	Y	
Provision 5.6-2	M	Y	
Provision 5.6-3	R	Y	
Provision 5.6-4	M C (13)	N/A	no debug interface accessible
Provision 5.6-5	R	Y	
Provision 5.6-6	R	Y	
Provision 5.6-7	R	Y	
Provision 5.6-8	R	N	
Provision 5.6-9	R	Y	
5.7 Ensure software integrity			
Provision 5.7-1	R	N	
Provision 5.7-2	R	N	
5.8 Ensure that personal data is secure			
Provision 5.8-1	R	N/A	no personal data transit through BESS hw/sw
Provision 5.8-2	M	Y	applicable to server/cloud services and to the customer App for mobile devices
Provision 5.8-3	M	Y	
5.9 Make systems resilient to outages			
Provision 5.9-1	R	Y	
Provision 5.9-2	R	Y	
Provision 5.9-3	R	Y	
5.10 Examine system telemetry data			
Provision 5.10-1	R C (6)	N	

EN 303 645 v2.1.1 (2020-06) Table B.1: Implementation of provisions for consumer IoT security			
Clause number and title			
Reference	Status	Support	Detail
5.11 Make it easy for users to delete user data			
Provision 5.11-1	M	N/A	
Provision 5.11-2	R	N/A	
Provision 5.11-3	R	N/A	
Provision 5.11-4	R	N/A	
5.12 Make installation and maintenance of devices easy			
Provision 5.12-1	R	Y	
Provision 5.12-2	R	Y	
Provision 5.12-3	R	Y	
5.13 Validate input data			
Provision 5.13-1	M	Y	
6 Data protection provisions for consumer IoT			
Provision 6.1	M	N/A	no personal data transit through BESS hw/sw
Provision 6.2	M C (7)	N/A	
Provision 6.3	M	N/A	
Provision 6.4	R C (6)	N/A	
Provision 6.5	M C (6)	N/A	
Conditions:			
1) passwords are used; 2) pre-installed passwords are used; 3) software components are not updateable; 4) the device is constrained; 5) the device is not constrained; 6) telemetry data being collected; 7) personal data is processed on the basis of consumers' consent; 8) the device allowing user authentication; 9) the device supports automatic updates and/or update notifications; 10) a hard-coded unique per device identity is used for security purposes; 11) updates are delivered over a network interface; 12) an update mechanism is implemented; 13) a debug interface is physically accessible.			
Status' Column:			
M	Mandatory provision		
R	Recommended provision		
M C	Mandatory and conditional provision		
R C	Recommended and conditional provision		
Support' Column:			
Y	Implemented		
N	Not implemented		
N/A	Not applicable		

宁波德业变频技术有限公司
NINGBO DEYE INVERTER TECHNOLOGY CO., LTD.

Bard Dai

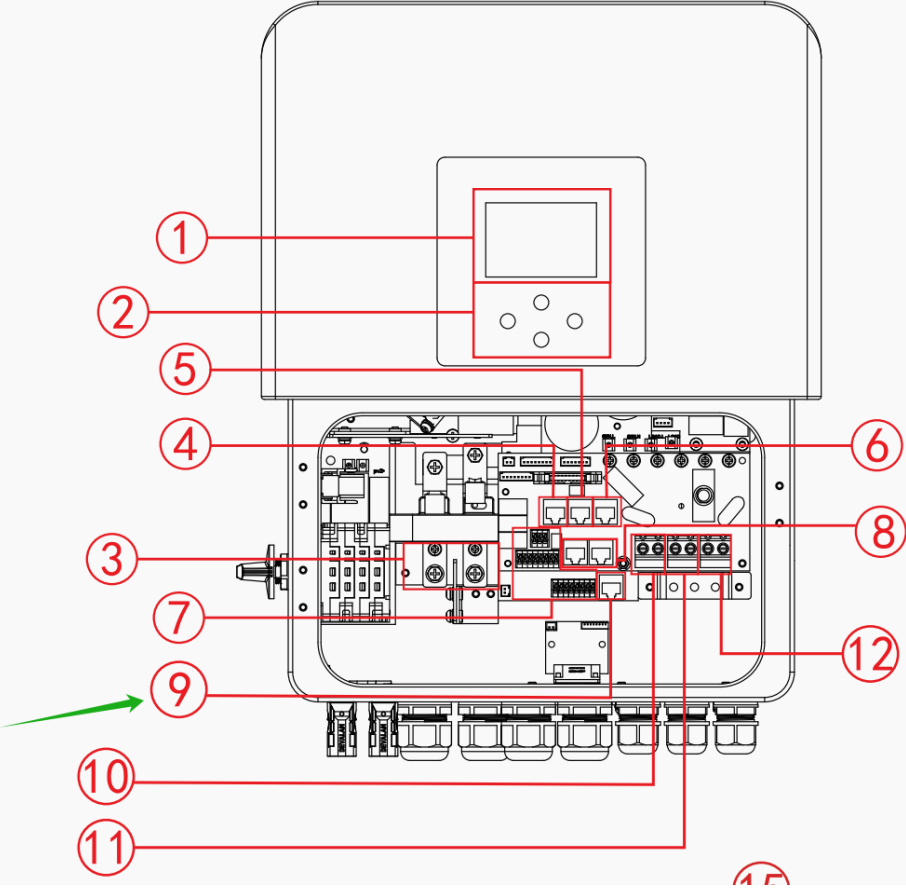
Senior Standard and Certification Engineer

On behalf of NingBo Deye Inverter Technology Co., Ltd.

December 3, 2021

Place: NingBo, China

Wiring functional tests if required by para. 15.2.1	N/A
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning).	N/A
Note. The inverter was tested in a test laboratory. The correct wiring functional test in the field has to be done by the responsible person for the installation of the plant.	

Logic Interface (input port) Required by paragraph 11.1.3.1	P
Confirm that an input port is provided and can be used to reduce the Active Power output to zero	Yes
Note. Manufacturer information provided.	
Provide high level description of logic interface, e.g. details in 11.1.3.1 such as AC or DC signal	Yes
Note. The inverter has a DRM logical port, A 15K resistor needs to be connected to PIN 5 of the DRM port. The connection between PIN 1 and PIN 6 can be controlled by a switch, When the switch is closed, the inverter will reduce the active power to 0 within 5 seconds. 	

No.	DRM Pin
1	DRM1/5
2	DRM2/6
3	DRM3/7
4	DRM4/8
5	REF-GEN/0
6	GND
7	NetDRM_7
8	NetDRM_7

DRM port

